Abstracts

A high efficiency and low distortion GaAs power MMIC design in the wide load impedance range by extended use of load-pull method

K. Ishida, H. Ikeda, H. Kosugi, M. Nishijima and T. Uwano. "A high efficiency and low distortion GaAs power MMIC design in the wide load impedance range by extended use of load-pull method." 1999 MTT-S International Microwave Symposium Digest 99.2 (1999 Vol. II [MWSYM]): 775-778 vol.2.

A new design technique by an extended use of the load-pull method for a high efficiency and low distortion power amplifier in wide range of load impedances is proposed. A two stage GaAs MMIC power amplifier which meets the Japanese PHS standard with high efficiency and low distortion within load VSWR of 3 was designed by using this method.

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